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PROGRAMMING METHODOLOGY AND ARCHITECTURE FOR A  
PROGRAMMABLE ANALOG SYSTEM

SUPPORT FOR AMENDMENTS

Support for the amendments herein can be found throughout the specification (e.g., page 4, lines 2-16; page 45, lines 9-16), Title and Abstract as originally filed. The present amendment intends to remove references to the trademarks of Cypress Microsystems, Inc. (see, e.g., M.P.E.P. § 608.01(v) and the attached printouts from [http://www.cypressmicro.com/corporate/CY\\_Announces\\_nov\\_13\\_2000.html](http://www.cypressmicro.com/corporate/CY_Announces_nov_13_2000.html) and <http://tess.uspto.gov/>, notably the "PSOC" trademark registration information therein.) No new matter is introduced.

REMARKS

Claims 1-29 are presented for consideration in the present application, which is now believed to be in condition for examination. Early notice to that effect is earnestly solicited.

Respectfully submitted,

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AMENDMENTS WITH CHANGES SHOWN:

IN THE TITLE

PROGRAMMING METHODOLOGY AND ARCHITECTURE FOR [AN] A  
PROGRAMMABLE ANALOG [PROGRAMMABLE] SYSTEM [ON A CHIP]

IN THE SPECIFICATION

At page 1, lines 1-2:

PROGRAMMING METHODOLOGY AND ARCHITECTURE FOR [AN] A  
PROGRAMMABLE ANALOG [PROGRAMMABLE] SYSTEM [ON A CHIP]

From page 4, line 18, through page 5, line 2:

In the present embodiment, the analog blocks are arranged in an array on a single integrated circuit, or chip. The analog system architecture can be generally referred to as [an] a programmable analog " [programmable] system-on-a-chip [,]" [or PSoC,] block. [PSoC] Such blocks can be used in those applications that typically require multiple chips that may be fabricated using different technologies. Implementation in embedded applications, including audio, wireless, handheld, data communications, Internet control, and industrial and consumer systems, is contemplated.

At page 13, line 10:

[AN] A PROGRAMMABLE ANALOG [PROGRAMMABLE] SYSTEM [ON A CHIP]  
ARCHITECTURE

CYPR-CD00174  
Serial No. 09/930,021

At page 33, lines 13-14:

PROGRAMMING METHODOLOGY AND ARCHITECTURE FOR [AN] A PROGRAMMABLE  
ANALOG [PROGRAMMABLE] SYSTEM [ON A CHIP]

At page 46, lines 15-20:

The preferred embodiment of the present invention, programming methodology and architecture for [an] a programmable analog [programmable] system [on a chip], is thus described. While the present invention has been described in particular embodiments, it should be appreciated that the present invention should not be construed as limited by such embodiments, but rather construed according to the following claims.

IN THE ABSTRACT

At page 54, lines 1-2:

PROGRAMMING METHODOLOGY AND ARCHITECTURE FOR [AN] A  
PROGRAMMABLE ANALOG [PROGRAMMABLE] SYSTEM [ON A CHIP]